Publikacje Mariusz Węgrzyn

Wegrzyn, M., Novak, F., Biasizzo, A. (2006). *,,Application-oriented testing of FPGA circuits."*42rd International Conference on Microelectronics Devices and Materials MIDEM, Strunjan, Slovenia.

Wegrzyn, M., Novak, F., Biasizzo, A., Renovell, M.(2007 A). *,,Functional test of processor cores in FPGA-based applications."*Proc.Workshop on Electronic Testing 43 rd International Conference on Microelectronics Devices and Materials MIDEM, Bled, 177-181.

Wegrzyn, M., Biasizzo, A., Novak, F. (2007 B). *,,Application-oriented testing of embedded processor cores implemented in FPGA circuits."*International Review on Computers and Software, 2**:** 666-671.

Wegrzyn, M., Biasizzo, A., Novak, F., Renovell, M., (2009). *,,Functional Testing of Processor Cores in FPGA-Based Applications."* Computing and Informatics, Vol.28, 2009, 97-113.

Wegrzyn, M., Sosnowski, J., (2014 A). *,,Tracing Fault Effects in FPGA Systems"* INTERNATIONAL JOURNAL OF ELECTRONICS AND TELECOMMUNICATIONS Vol.60, NO. 1, 103-108.

Wegrzyn, M., Sosnowski, J.,(2014 B). *,,Testing schemes for systems based on FPGAs processor cores"* Pomiay, Automatyka, Kontrola Vol. 56, nr 01, 483-485.